

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hirokazu HONDA

Title: METHOD OF MANUFACTURING A FLIP-CHIP
SEMICONDUCTOR DEVICE WITH A STRESS-ABSORBING
LAYER MADE OF THERMOSETTING RESIN

Appl. No.: Unassigned

Filing Date: October 17, 2003

Examiner: Unassigned

Art Unit: 2824

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Mail Stop PATENT APPLICATION
Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

Applicant submits herewith on Form PTO/SB/08 a listing of the documents cited by or submitted to the U.S. PTO in parent application Serial No. 09/704,521, filed 11/03/2000. As provided in 37 CFR §1.98(d), copies of the documents are not being provided since they were previously submitted to the United States Patent & Trademark Office in the above-identified parent application.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the filing date of the application.

RELEVANCE OF EACH DOCUMENT

The relevance of the documents is explained in the parent application.

Applicant respectfully requests that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date: October 17, 2003

FOLEY & LARDNER
Customer Number: 22428
Telephone: (202) 672-5407
Facsimile: (202) 672-5399

By Philip J. Articola

Reg. No.
38,819

for / David A. Blumenthal
Attorney for Applicant
Registration No. 26,257

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Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	Unassigned
Date Submitted: October 17, 2003 <i>(use as many sheets as necessary)</i>				Filing Date	October 17, 2003
				First Named Inventor	Hirokazu HONDA
				Group Art Unit	2824
				Examiner Name	Unassigned
Sheet	1	of	2	Attorney Docket Number	067123-0195

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
A1	6,153,448			TAKAHASHI et al.	09-06-2001	
A2	5,998,861			HIRUTA	12-02-1999	
A3	5,977,641			TAKAHASHI et al.	11-02-1999	
A4	5,736,790			IYOGI et al.	04-07-1998	
A5	5,677,576			AKAGAWA	10-14-1997	
A6	5,682,061			KHANDROS et al.	10-28-1997	
A7	2001/0018986			NAGAI et al.	09-06-2001	
A8	2001/0013425			ROKUGAWA et al.	08-16-2001	
A9	2001/0004134			SAITO	06-21-2001	
A10	5,683,942			KATA et al.	11-04-1997	
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A16	5,998,861			HIRUTA	12-07-1999	

FOREIGN PATENT DOCUMENTS

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		Office ³	Number ⁴				
A17	EP	1 043 922		A1	IBIDEN CO., LTD.	10-11-2000	
A18	WO	99/17352		A1	PARTNERSHIPS LIMITED, INC.	04-08-1999	
A19	EP	1 128 431		A2	NEC CORPORATION	08-29-2001	
A20	WO	98/25297		A1	SEIKO EPSON CORPORATION	06-11-1998	
A21	JP	2001-24021			HITACHI LTD.	01-26-2001	
A22	JP	5-275489			HITACHI LTD.	10-22-1993	
A23	JP	8-330355			SHINKO ELECTRIC IND. CO. LTD.	12-13-1996	
A24	JP	8-37253			MITSUBISHI ELECTRIC CORP.	02-06-1996	
A25	JP	9-148334			TOSHIBA CORP.	06-06-1997	
A26	JP	3-119737			FUJITSU LTD.	05-22-1991	
A27	JP	10-313074			TOSHIBA CORP.	11-24-1998	
A28	JP	11-097578			HITACHI LTD.	04-09-1999	
A29	JP	11-224885			MATSUSHITA ELECTRON CORP.	08-17-1999	
A30	JP	11-111896			NEC CORPORATION	04-23-1999	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	A31	JP	06-302604		MITSUBISHI ELECTRIC CORP.	10-28-1994		
	A32	JP	2000-349197		MATSUSHITA ELECTRIC CO., LTD.	12-15-2000		
	A33	JP	2000-174052		IBIDEN CO. LTD.	06-23-2000		
	A34	JP	08-330313		SHINKO ELECTRIC IND. CO., LTD.	12-13-1996		
	A35	JP	2001-028379		ASAHI CHEM IND. CO. LTD.	10-30-2001		
	A36	JP	2000-269256		HITACHI LTD.	09-29-2000		
	A37	JP	10-303327		YAMAICHI ELECTRON CO., LTD.	11-13-1998		
	A38	WO	99/18609		MINNESOTA MINING AND MANUFACTURING COMPANY	04-15-1999		
	A39	JP	5-503191		EPOXY TECHNOLOGY INC.	05-27-1993		
	A40	JP	09-092685		TOSHIBA KK	04-04-1997		
	A41	JP	11-040613	A	SHINKO ELECTRIC IND. CO. LTD.	02-12-1999		
	A42	JP	11-074309	A	TOSHIBA CORP.	03-16-1999		

NON PATENT LITERATURE DOCUMENTS								
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.					T ⁶	
	A43	AMAMI et al., "Advanced MCM-Ls for Consumer Electronics," IEMT/IMC Proceeding, pp. 249-254, Matsushita Electric Industrial Co., Ltd. 1998.						
	A44	KOJI MATSUI et al., "Resin and Flexible Metal Bumps for Chip-on-Glass Technology," Electronic Components and Tech. Conf., 1993 Proceedings, 43 rd , IEEE, Material Development Center, Color LCD Division, NEC Corporation.						

Examiner Signature		Date Considered
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